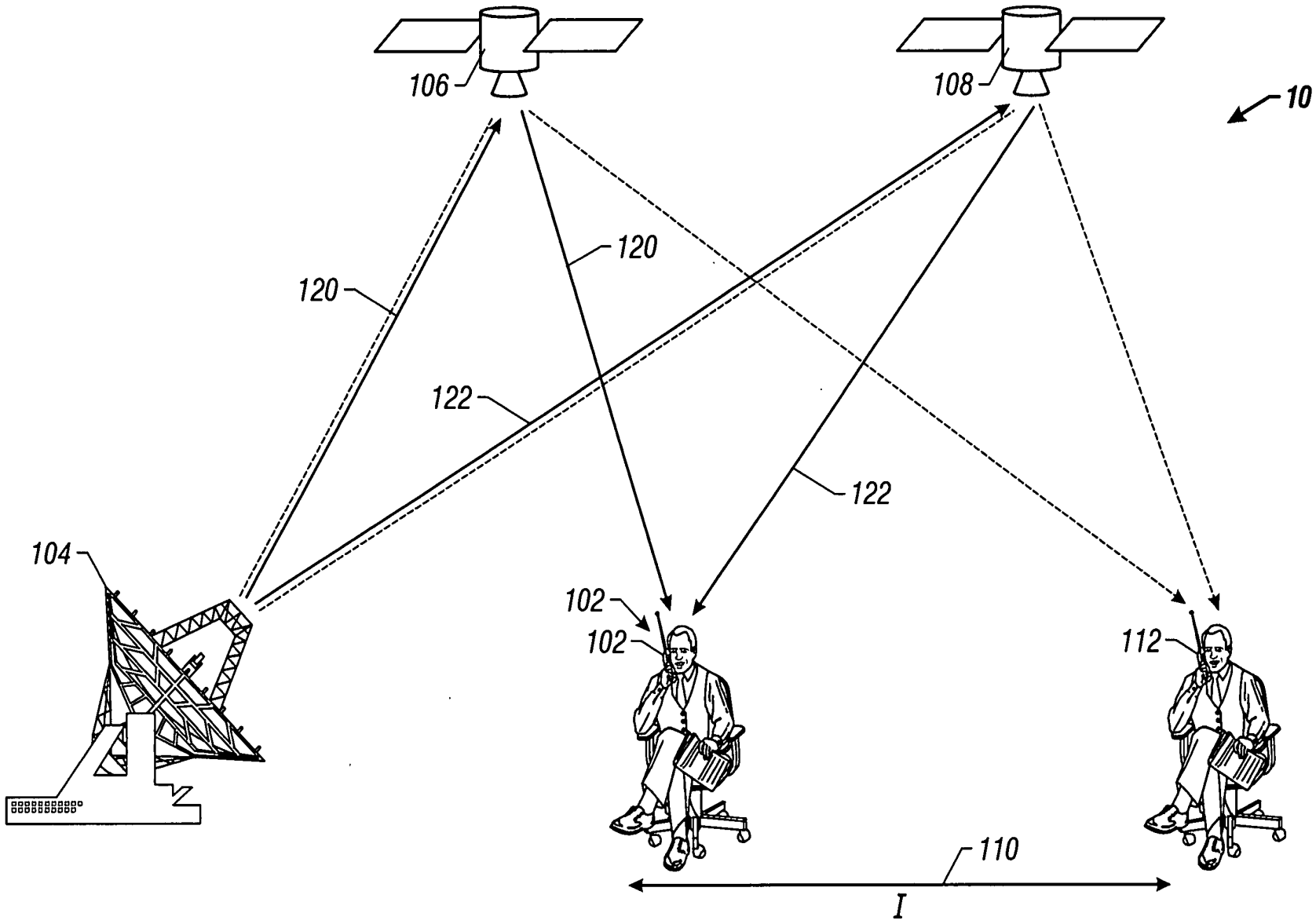

FIG. 1

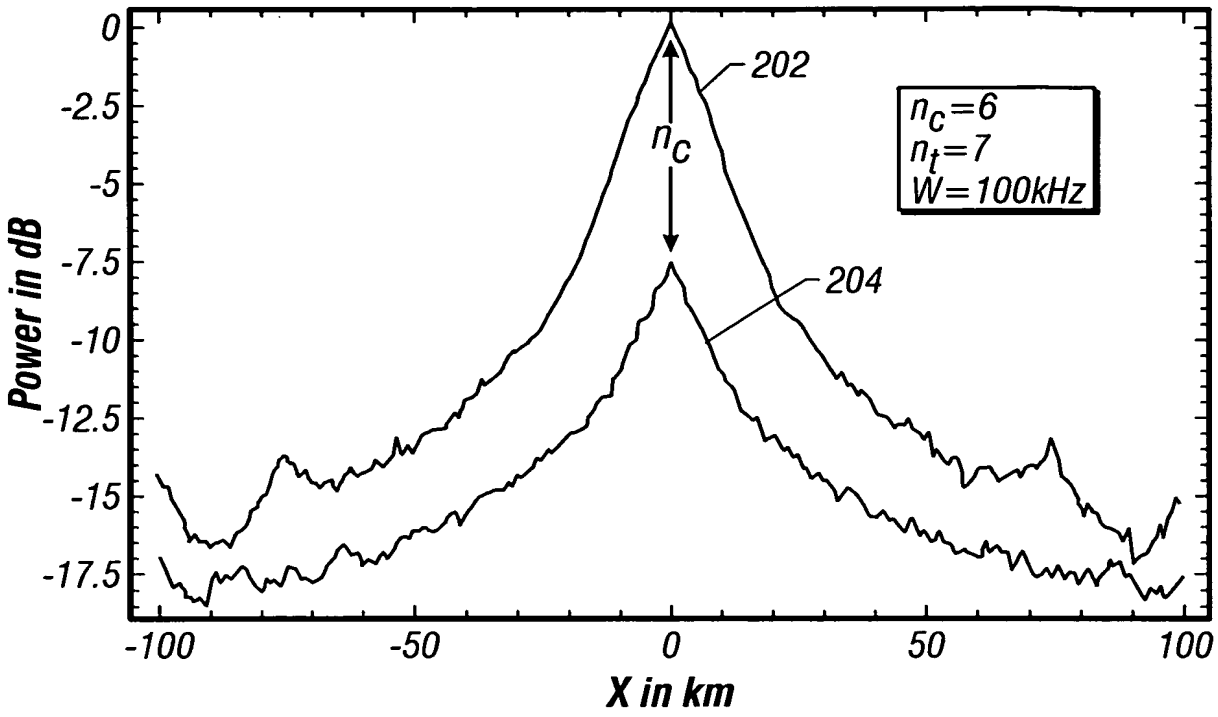
0
-2.5
-5
-7.5
-10
-12.5
-15
-17.5
Power in dB
-100
-50
0
50
100
X in km
202
204
n_c
n_c=6
n_t=7
W=100kHz

FIG. 2

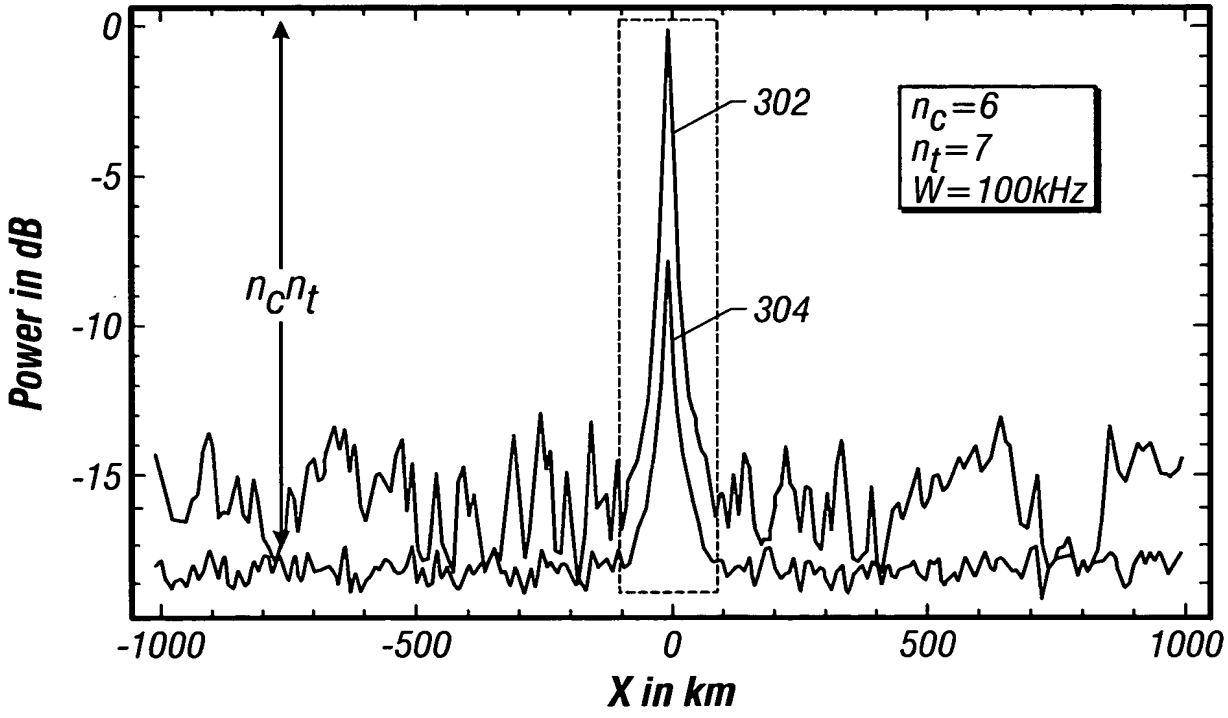
0
-5
-10
-15
Power in dB
-1000
-500
0
500
1000
X in km
302
304
$n_c n_t$
n_c=6
n_t=7
W=100kHz

FIG. 3

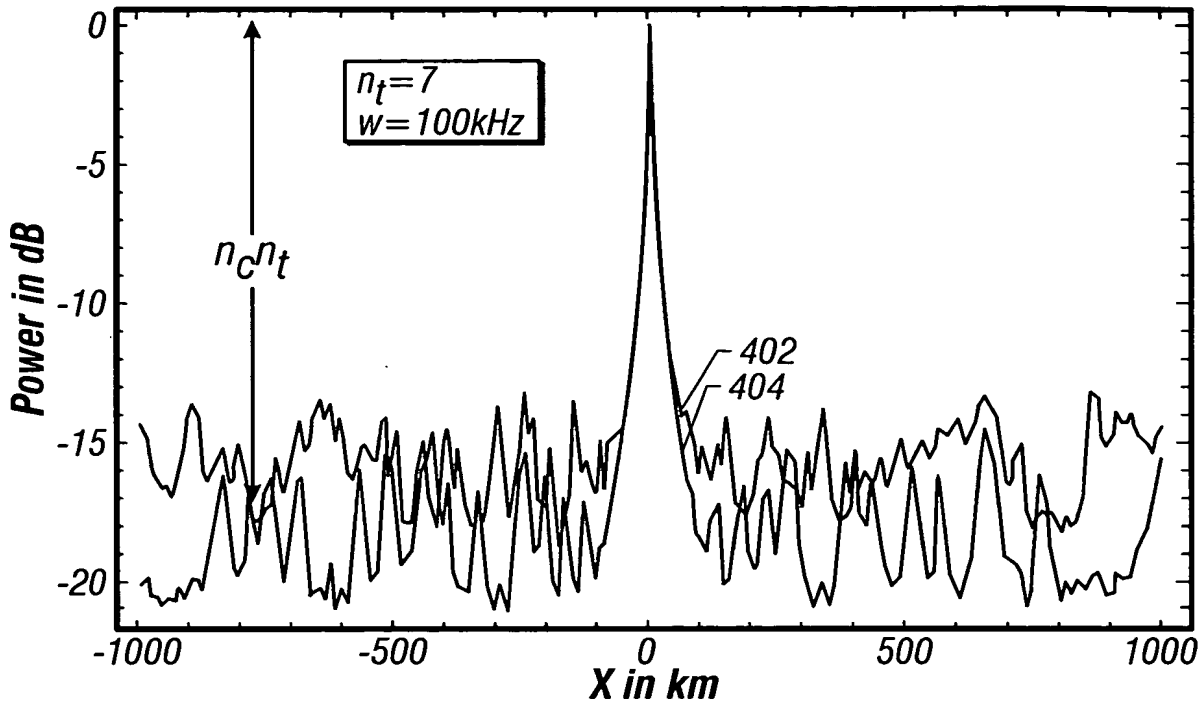

FIG. 4

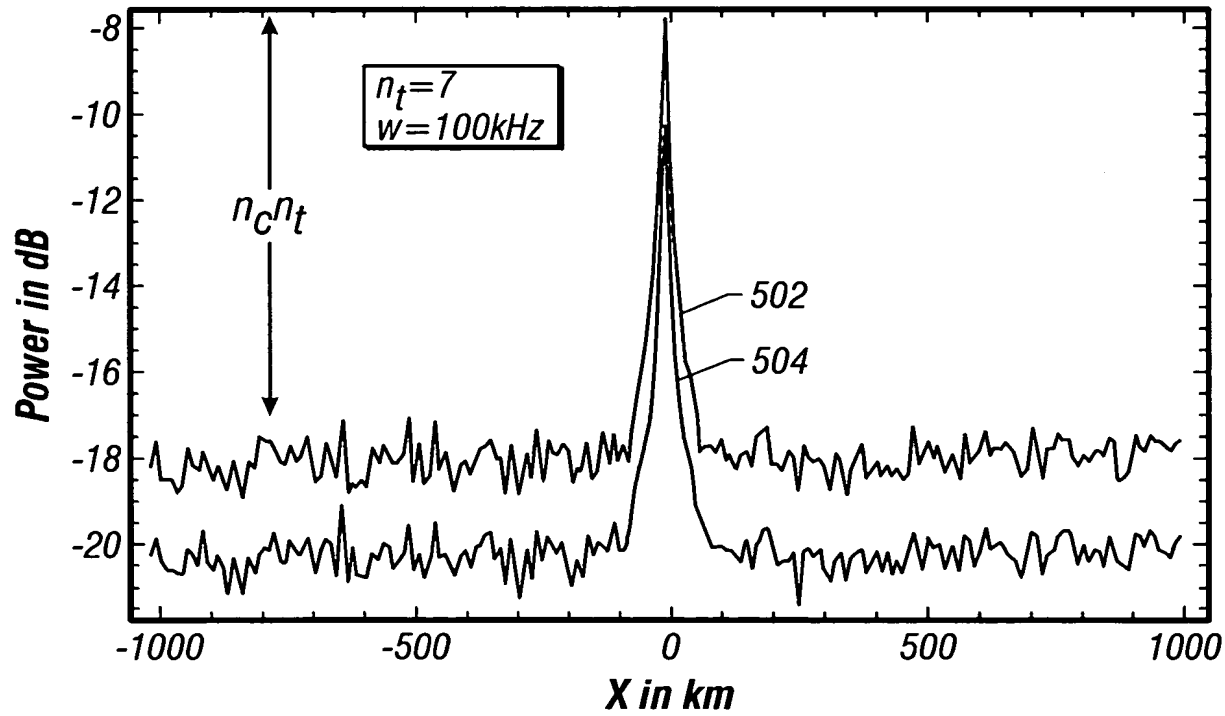

FIG. 5

0027h0 - 5050560

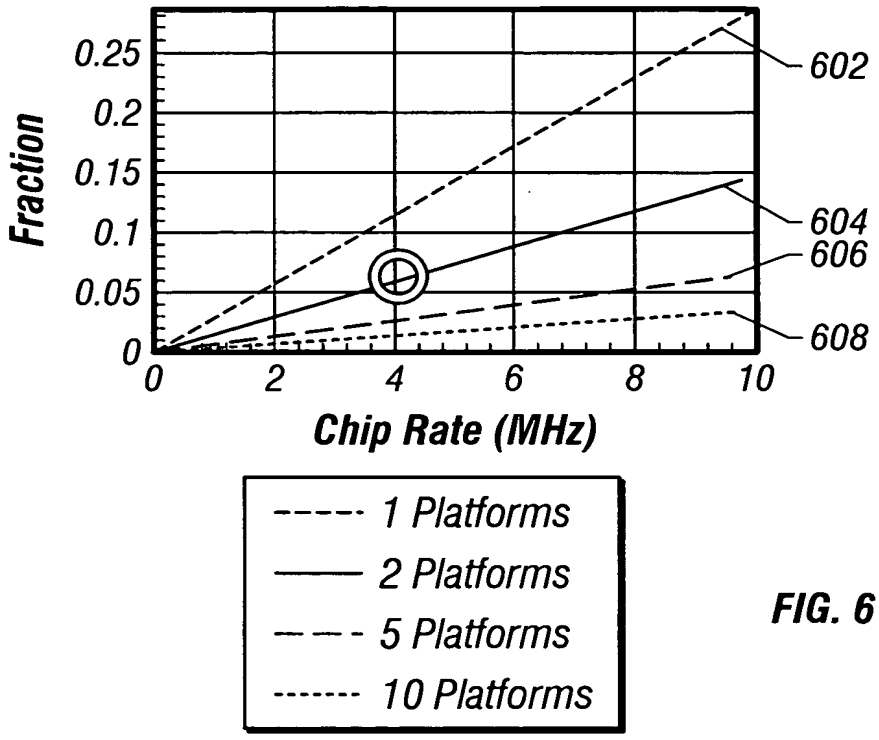
0.25
0.2
0.15
0.1
0.05
0
Fraction
0
2
4
6
8
10
Chip Rate (MHz)
602
604
606
608
1 Platforms
2 Platforms
5 Platforms
10 Platforms

FIG. 6

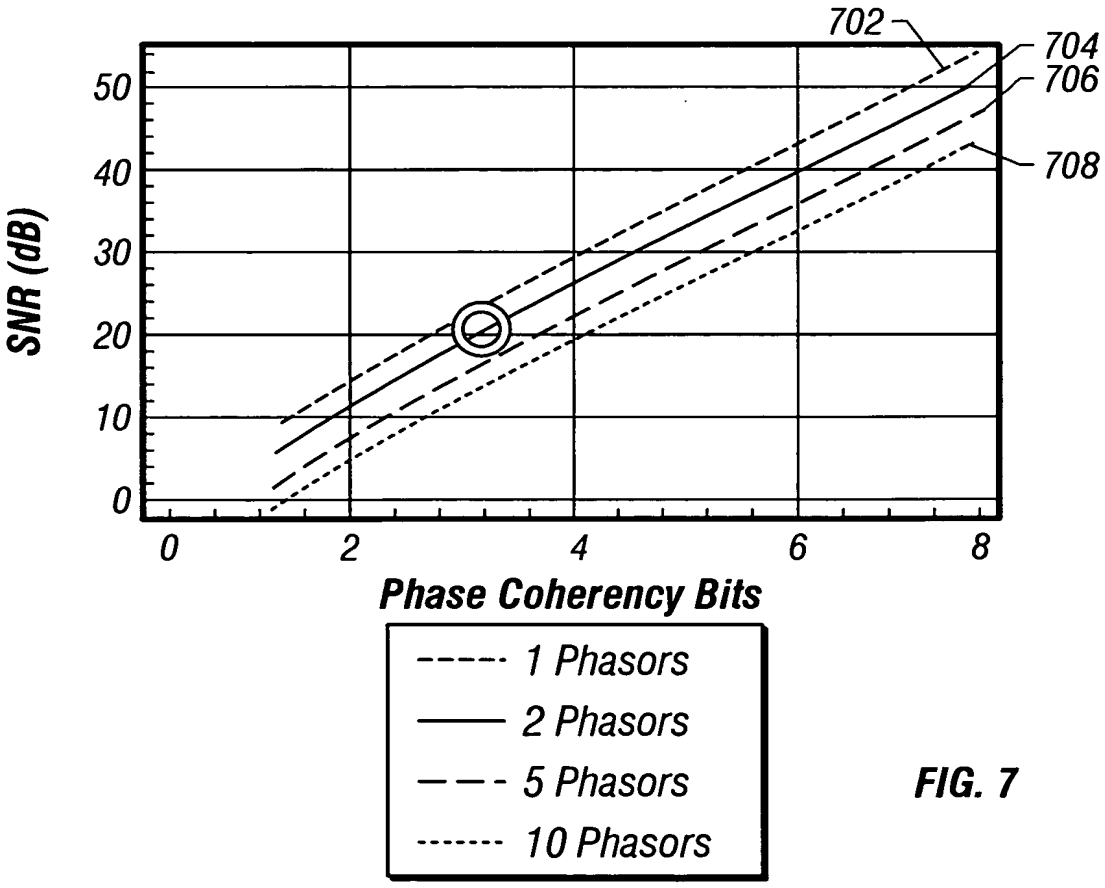
50
40
30
20
10
0
SNR (dB)
0
2
4
6
8
Phase Coherency Bits
702
704
706
708
1 Phasors
2 Phasors
5 Phasors
10 Phasors

FIG. 7

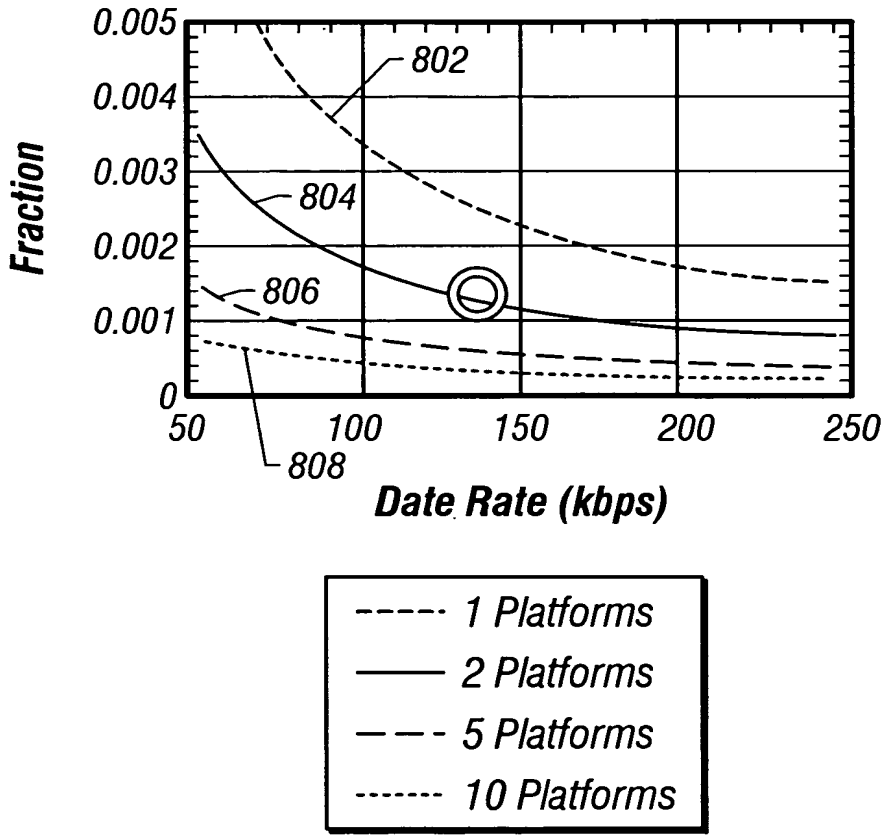

FIG. 8

0074"5055560

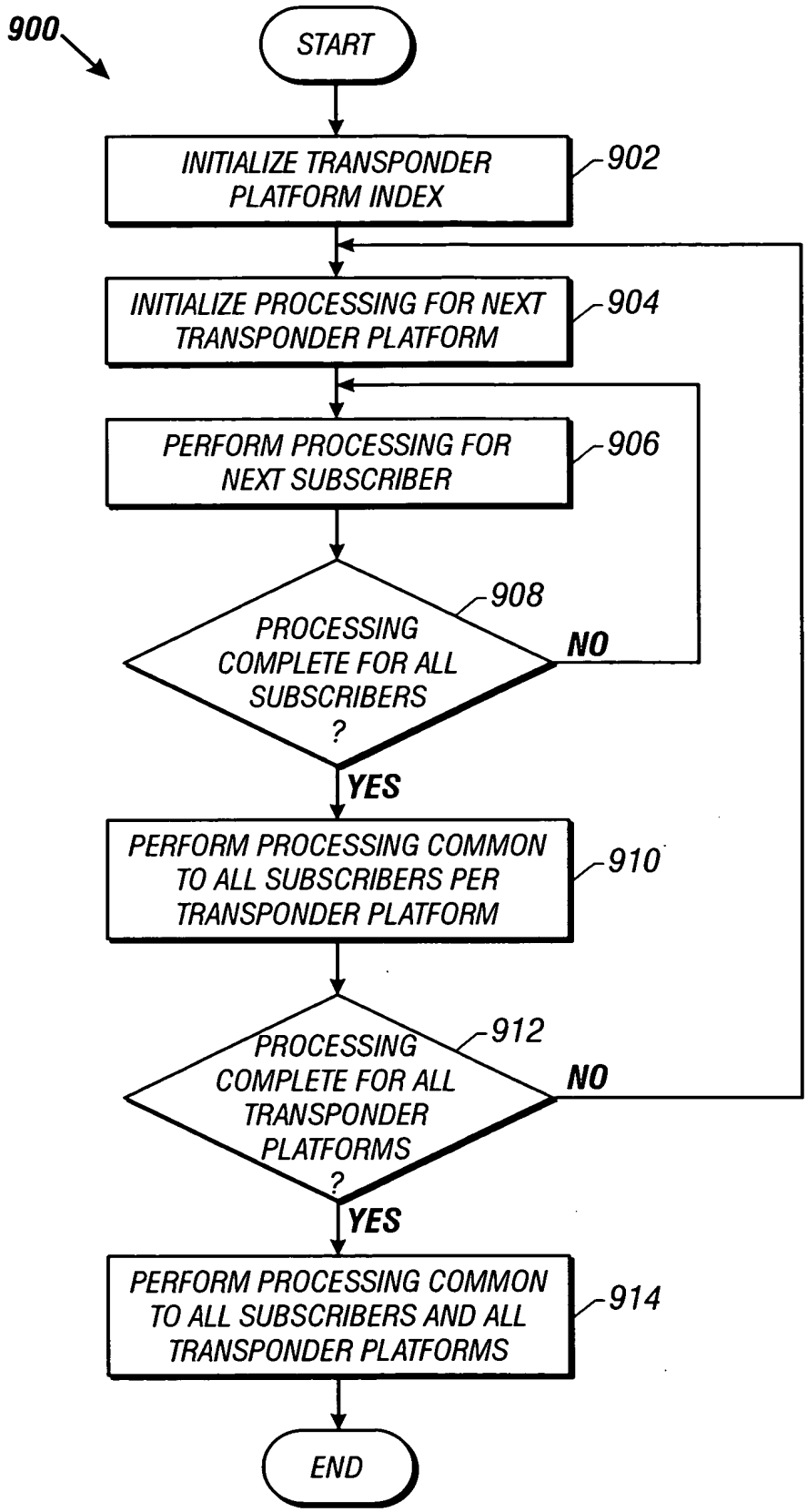
900
START
INITIALIZE TRANSPONDER PLATFORM INDEX
902
INITIALIZE PROCESSING FOR NEXT TRANSPONDER PLATFORM
904
PERFORM PROCESSING FOR NEXT SUBSCRIBER
906
PROCESSING COMPLETE FOR ALL SUBSCRIBERS ?
908
NO
YES
PERFORM PROCESSING COMMON TO ALL SUBSCRIBERS PER TRANSPONDER PLATFORM
910
PROCESSING COMPLETE FOR ALL TRANSPONDER PLATFORMS ?
912
NO
YES
PERFORM PROCESSING COMMON TO ALL SUBSCRIBERS AND ALL TRANSPONDER PLATFORMS
914
END

FIG. 9

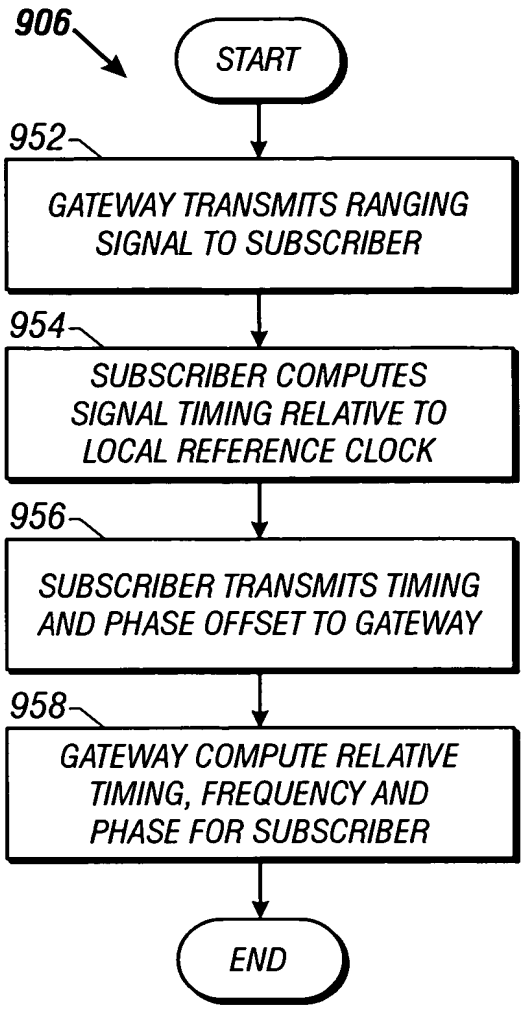

FIG. 9A

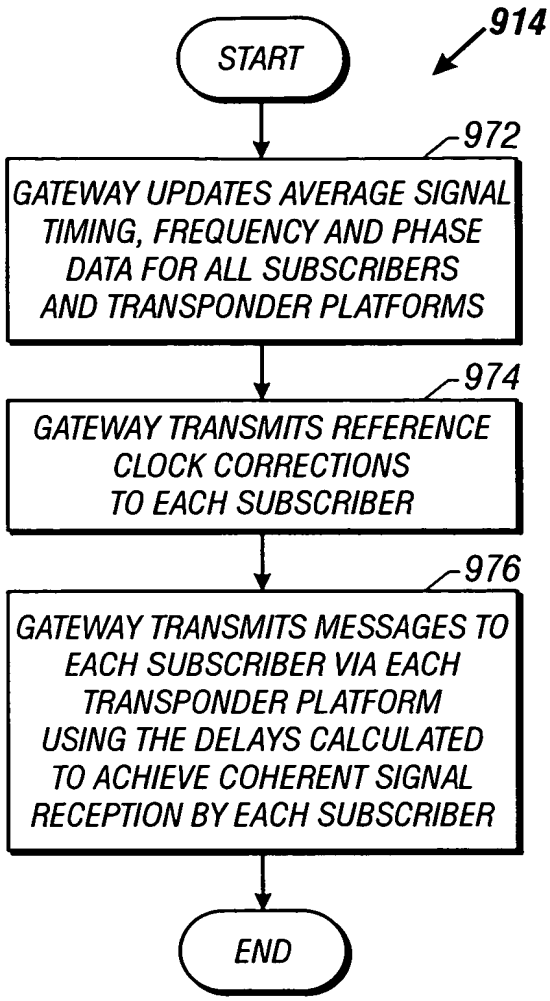

FIG. 9C

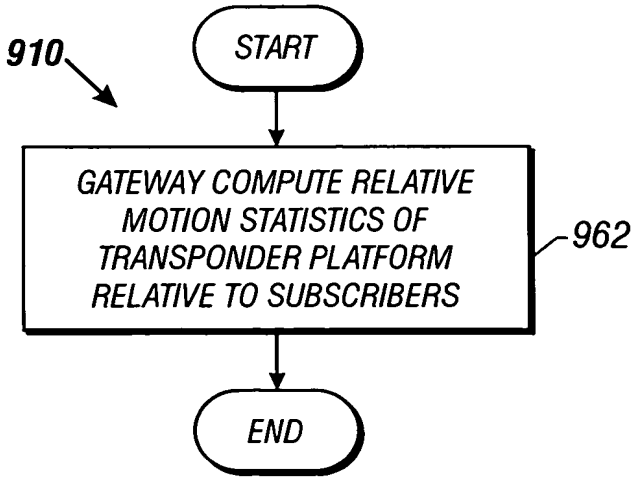

FIG. 9B

002 THD 5050560

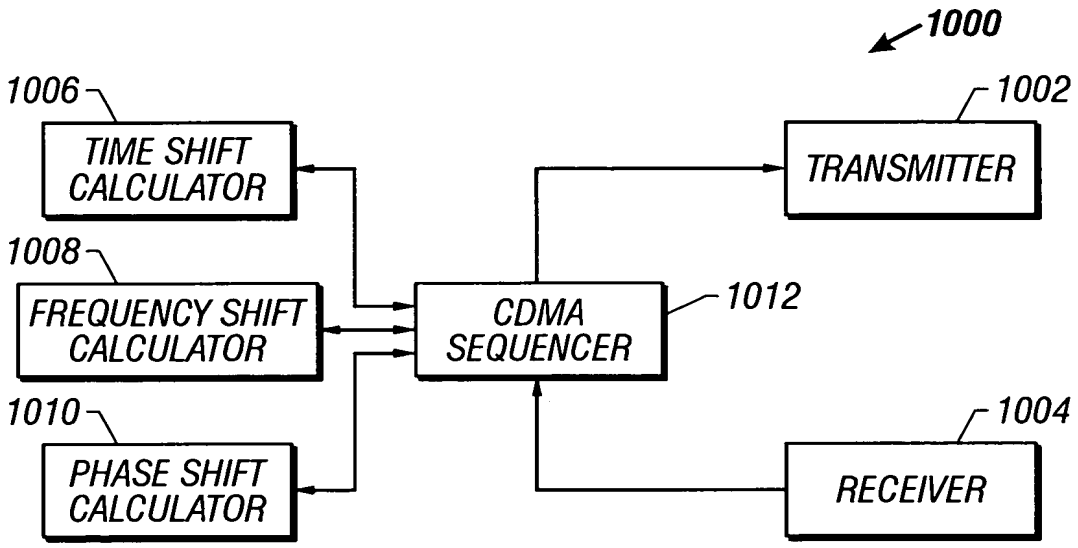

FIG. 10

0027+0-5055560